

**COLLEGE OF ENGINEERING ROORKEE (COER)**  
**ODD SEMESTER(Session 2017-2018) TIME TABLE**  
**B.TECH 3rd YEAR, V SEMESTER**

w.e.f. July 27, 2017  
Section: G

Branch:-AEI

Time Days → ↓	I 9.00-9.55	II 10.00-10.55	III 11.00-11.55	IV 12.00-12.55	V 1.00-1.55	VI 2.00-2.55	VII 3.00-3.55	VIII 4.00-4.55
Monday		L- TCS-507 (VK) CE - 303	L- TEC 501 (ADA) CE-303	LUNCH	T-TEC 504 CE- 407, MND	L- TEC-503 (PKM) CE - 303	VAP	
Tuesday		L- TEC 501 (ADA) CE-303	L- TCS-507 (VK) CE - 303	LUNCH	L- TEC-503 (PKM) CE - 303	L-TIC-505 (AV) CE - 407	P2-PCS 554,CC1, (VK)	
Wednesday	PEC 551, L302, (BP)		LIB	LUNCH	L-TEC-504 (MND) CE - 303	L- TEC-503 (PKM) CE - 303	L- TEC-502 (NTS) CE - 303	
Thursday	L- TEC 501 (ADA) CE-303	L- TEC-502 (NTS) CE - 303	L- TCS-507 (VK) CE - 303	LUNCH	T-TEC 502 CE- 303, NTS	T-TEC 501 CE- 303, ADA	VAP	
Friday		T-TCS 507 CE 304, VK	L-TEC-504 (MND) CE - 303	LUNCH	L- TEC-503 (PKM) CE - 303	L- TEC-502 (NTS) CE - 303	L-TIC-505 (AV) CE - 303	PEC-552 (SGD)-L-201 (B)
Saturday	L-TIC-505 (AV) CE - 407	L-TEC-504 (MND) CE - 303						

TEC-501 Automatic Control System- Mr.Adesh Arya (ADA)  
TEC- 502 Digital Signal Processing – Mr. Nitin Sharma (NTS)  
TEC- 503 VLSI Technology – Mr. Puneet Kumar Mishra(PKM)  
TEC-504 Advanced Microprocessor- Mr. Mayank Dev (MND)  
TIC-505 Transducers, Sensors and Display Devices Ms. Ashita Vermani(AV)  
TCS-507 OOPS-Mr. Vineet Kumar (VNK)

PEC-551Advanced Microprocessor Lab- Ms. Bharti Papnai (BP)  
PEC-552 Digital Signal Processing Lab – Dr. Sagarika Das(SGD)  
PCS-554 OOPS- Mr. Vineet Kumar (VK)

(ASHITA VERMANI)  
TIME TABLE O/C

(B.D. PATEL)  
H.O.D.

(A.S.YADAV)  
DEAN ACADEMICS