

**COLLEGE OF ENGINEERING ROORKEE (COER)**  
**ODD SEMESTER(Session 2017-2018) TIME TABLE**  
**B.TECH 3rd YEAR, V SEMESTER**

w.e.f. July 27, 2017  
Section: J

Branch:-ET

Time Days → ↘	I 9.00-9.55	II 10.00-10.55	III 11.00-11.55	IV 12.00-12.55	V 1.00-1.55	VI 2.00-2.55	VII 3.00-3.55	VIII 4.00-4.55
<b>Monday</b>	L-TEC-505 (ARK) CE - 304	L-TEC-504 (MND) CE - 304	L- TCS-507 (VK) CE - 304	<b>LUNCH</b>	T1-TEC 501 CE- 304, ADA T2-TCS 507 CE- 302, VK	T1-TCS 507 CE- 304, VK T2-TEC 501 CE- 302, ADA	VAP	
<b>Tuesday</b>	L- TEC-502 (NTS) CE - 304	L- TEC-503 (PKM) CE - 304	L-TEC-504 (MND) CE - 304	<b>LUNCH</b>	L- TCS-507 (VK) CE - 304	L- TEC 501 (ADA) CE-304	P1-PEC-552 (SGD), L201 B	
<b>Wednesday</b>	L- TEC-502 (NTS) CE - 304	L- TEC 501 (ADA) CE-304	L- TEC-503 (PKM) CE - 304	<b>LUNCH</b>	L- TCS-507 (VK) CE - 304	L-TEC-505 (ARK) CE - 304	P1-PCS-554 CC1 (VK) P2-PEC-552, (SGD), L201 B	
<b>Thursday</b>	L- TEC-503 (PKM) CE - 304	L-TEC-504 (MND) CE - 304	L- TEC 501 (ADA) CE-304	<b>LUNCH</b>	P1-PEC-551 (MND)-L-302 P2-PCS-554 (VK)-CC1		VAP	
<b>Friday</b>	P2-PEC-551 , (MND)-L-302		L-TEC-505 (ARK) CE - 304	<b>LUNCH</b>	L- TEC-502 (NTS) CE - 304			
<b>Saturday</b>	L- TEC-503 (PKM) CE - 304	L-TEC-505 (ARK) CE - 304	T1-TEC 502 CE 304, (NTS) T2-TEC 504 CE 302, (MND)	T1-TEC 504 CE 304, (MND) T2-TEC 502 CE 302, (NTS)				

TEC-501 Automatic Control System- Mr.Adesh Arya (ADA)  
TEC- 502 Digital Signal Processing – Mr. Nitin Sharma (NTS)  
TEC- 503 VLSI Technology – Mr. Puneet Kumar Mishra(PKM)  
TEC-504 Advanced Microprocessor- Mr. Mayank Dev (MND)  
TEC-505 Antenna and Wave Propagation Mr. Arvind Kumar(ARK)  
TCS-507 OOPS-Mr. Vineet Kumar (VNK)

PEC-551 Advanced Microprocessor Lab- Mr. Mayank Dev (MND)  
PEC-552 Digital Signal Processing Lab – Dr. Sagarika Das(SGD)  
PCS-554 OOPS- Mr. Vineet Kumar (VK)

(ASHITA VERMANI)  
TIME TABLE O/C

(B.D. PATEL)  
H.O.D.

(A.S.YADAV)  
DEAN ACADEMICS